



20-21 October 2016, Poznan, Poland

IEICE Workshop on Networks Optimization and Dimensioning

program

2016-10-18

Thursday 20th of October 2016

10:00 || Opening

10:00–12:00 || Session 1

1. Monika Grajzer – IPv6 address auto-configuration for wireless mobile ad hoc networks
2. Damian Kmieciak – Overflow of Elastic Traffic | [pdf](#)
3. Joanna Dębowska – Energy efficient content distribution in Content Delivery Network
4. Bartosz Musznicki– Role of Mobile Agents in Wireless Networks

12:00–12:15 || Coffee & tea break

12:15–14:15 || Session 2

1. Marcin Markowski – Fragmentation-Aware Strategies for Deadline-Driven Dynamic Multicast Scheduling Problem in EON
2. Mateusz Żotkiewicz – Rational Selection of Splitters and OLT Cards in FTTH Deployment | [pdf](#)
3. Grzegorz Rzym - Analysis of PCE-based path optimization in multi-domain SDN/MPLS/BGP-LS network | [pdf](#)
4. Grzegorz Rzym - Dynamic Traffic Management for minimization of inter-domain traffic cost in the Inter-cloud Communication | [pdf](#)

14:15–15:15 || Lunch

15:15–17:30 || Session 3

1. Piotr Boryło - Energy-Aware Fog and Cloud Interplay Supported by Wide Area Software Defined Networking | [pdf](#)
2. Piotr Jurkiewicz - Implementacja programowego rutera FAMTAR | [pdf](#)
3. Mustafa Abdulsahib, Wojciech Kabaciński, Marek Michalski – Control algorithm for setting up connections in switching fabrics for elastic optical networks
4. Marek Michalski - Algorithms for setting up and rearrangement of connection and their implementation In FPGA
5. Michał Czerwiński - The Two-Napkin Protocol in enterprise core design

17:30–17:45 || Coffee & tea break

17:45–19:45 || Session 4 (Special): SPIRENT – General Overview | [pdf](#)

Friday 21st of October 2016

9:00-11:00 || Session 5 (Special)

- 9:00-10:00 SPIRENT – Cyberflood | [pdf](#)
- 10:00-11:00 Gigamon Unified Network Visibility | [pdf](#)

11:00–11:15 || Coffee & tea break

11:15–14:15 || Session 6

1. Krzysztof Rusek - Projektowanie niezawodnych sieci telekomunikacyjnych w oparciu o zarządzanie ryzykiem | [pdf](#)
2. Agnieszka Rudnik, Anna Koralewska - Prezentacja projektów NCN, NCBiR oraz H2020 | [pdf](#) | [pdf](#)
3. Marek Michalski, Tytus Sielach – A proposal of a new structure for NetFPGA cards
4. Marek Michalski, Tytus Sielach – Projects with NetFPGA cards – NetFPGA workshop, ALIEN, OpenFlow testbed
5. Piotr Boryło – Projects STAR and SDNRoute | [pdf](#)
6. Maciej Piechowiak – I-Scover - IEICE Europe Section Experience | [pdf](#)

14:15 || Closing

14:15–15:15 || Lunch